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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/714,891 11/17/2000 PAT 6,646,287 *D*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 11-330174 11/19/1999 *D*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 11/20/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	JAPAN	30	13	4
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## TITLE

Semiconductor device and manufacturing method thereof

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